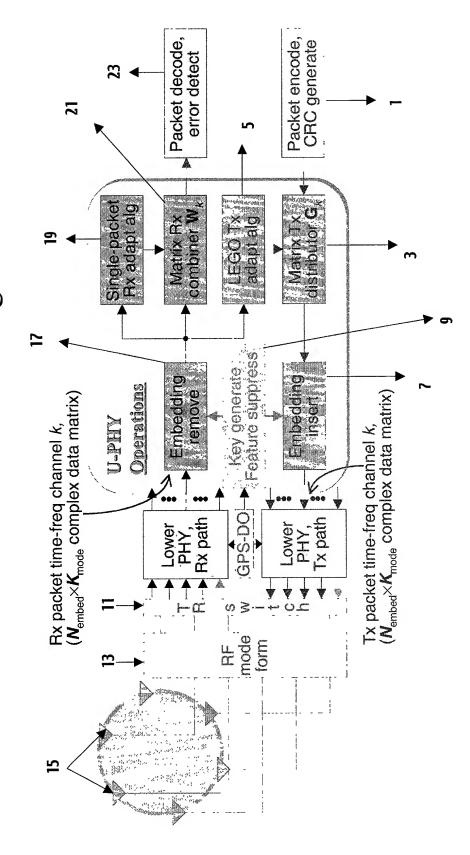
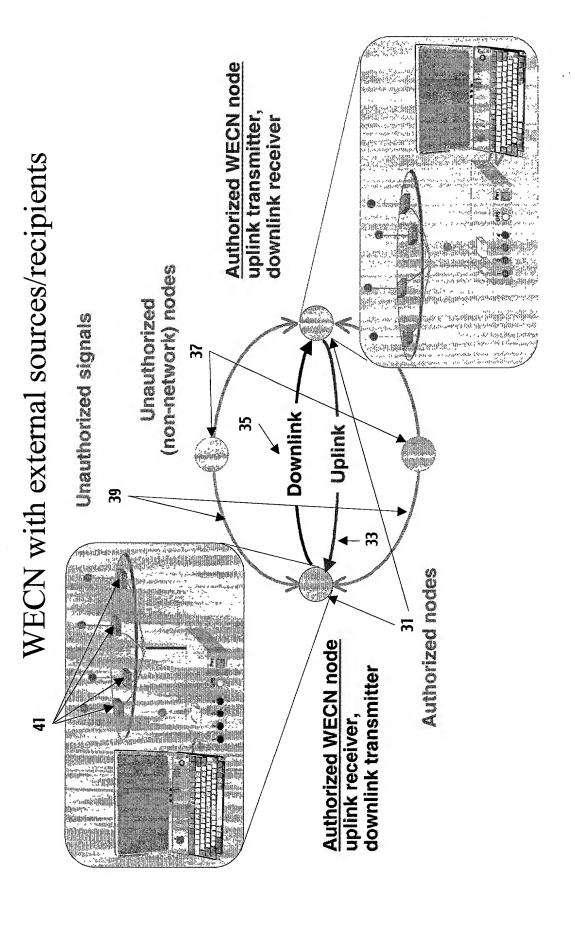
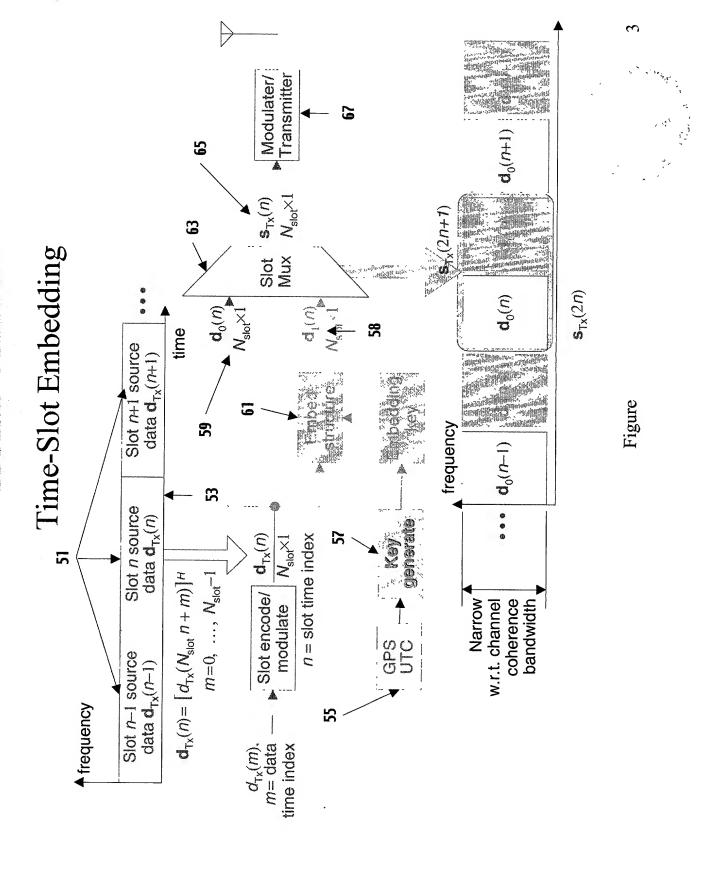
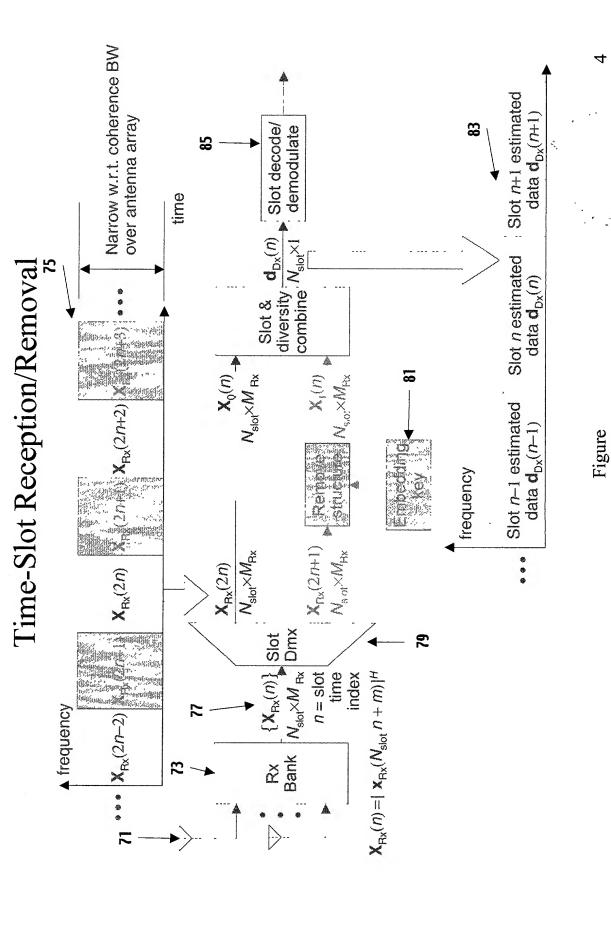
WECN Structural Embedding/Removal



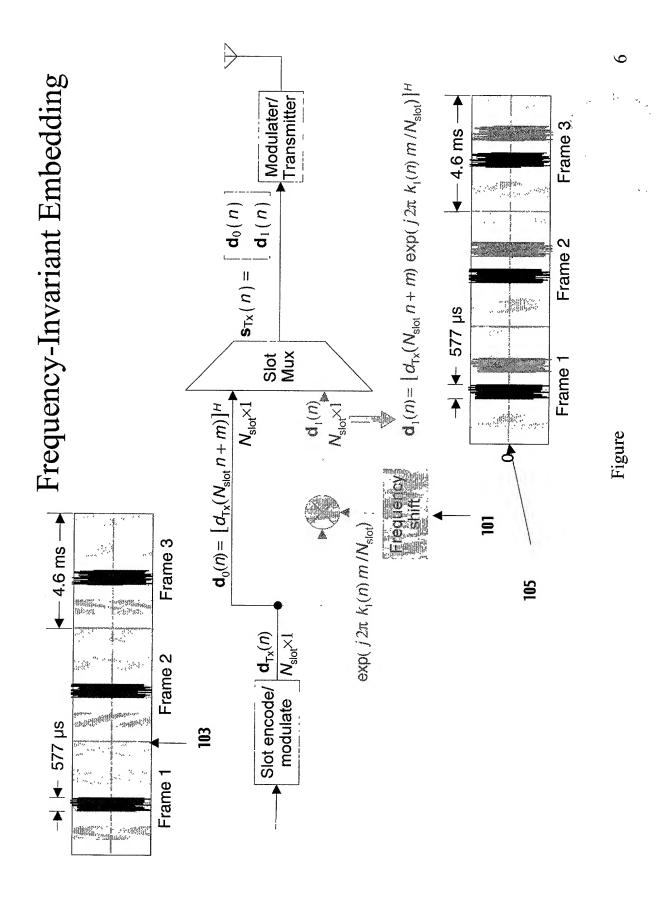
Figure

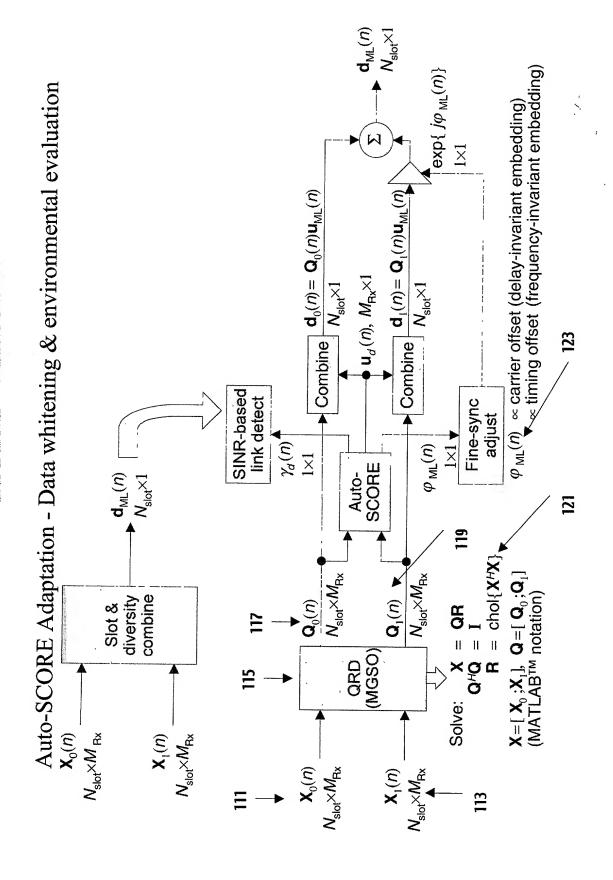






Modulater/ Transmitter 92 $\mathbf{d}_{1}(n) = \left[d_{\mathrm{rx}}(N_{s_{\mathrm{cl}}} n + mod\{n - m_{1}(n), N_{s,\mathrm{cl}}\}) \right]^{H}$ Frame 3 Frame 2 Delay-Invariant Embedding Frame 1 $\mathbf{d}_0(n) = \left[d_{\mathrm{Tx}} (N_{\mathrm{slot}} \, n + m) \right]^H$ $N_{\rm slot} \times 1$ 0 Frame 3 Su Slot encode/ $\mathbf{d}_{\mathrm{Tx}}(n)$ modulate $N_{\mathrm{slot} \times 1}$ Frame 2 Slot duration Frame 1 Y A





Single-Link Auto-SCORE Algorithm - Software

Combine $N_1 \times M$ matrices X_0 and X_1 into single $2N \times M$ matrix X,

$$\mathbf{X} = [\mathbf{X}_0^H \ \mathbf{X}_1^H]^H$$

where $N = N_{\text{slot}}$, $M = M_{\text{Rx}}$ if time-slot embedding is employed at the transmitter. Compute **QR** decomposition of X,

$$X = QR$$

$$\mathbf{Q}^H \mathbf{Q} = \mathbf{I}$$
$$\mathbf{R} = \operatorname{chol}\{\mathbf{X}^H \mathbf{X}\},\,$$

where Q is defined by

$$\mathbf{Q} = [\mathbf{q}_1 \dots \mathbf{q}_M]$$
$$= [\mathbf{q}(1) \dots \mathbf{q}(N)]^H$$

Separate Q into $N \times M$ submatrices Q_0 and Q_1 , such that

$$O_{\rm c} = X_{\rm c}$$

$$\mathbf{Q}_0 = \mathbf{X}_0 \mathbf{C}$$
$$\mathbf{Q}_1 = \mathbf{X}_1 \mathbf{C},$$

where $C = \mathbb{R}^{-1}$. Form $M \times M$ cross-correlation matrix S,

$$S = (1/N) Q_0^H Q$$

 $\mathbf{S} = (1/N) \mathbf{Q}_0^H \mathbf{Q}_1$

Initialize whitened linear combiner weights $\mathbf{u} = [s(m,M)]$ $v = ||\mathbf{u}|| (L_2 \text{ norm})$ $\mathbf{u} \leftarrow \mathbf{u}/v$

Iteratively update combiner weights (preset iterations, or until stopping criterion met) $\mathbf{v} = \mathbf{S}\mathbf{u}$ $\rho = 1/2 \operatorname{sign} \{\mathbf{v}^H \mathbf{u}\}$ $\mathbf{u} \leftarrow \rho \mathbf{v} + \rho^* \mathbf{S}^H \mathbf{u}$ $\mathbf{v} = ||\mathbf{u}|| \ (\mathbf{L}_2 \text{ norm})$

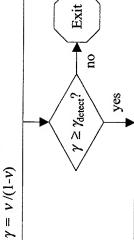
$$\mathbf{n} = 1/2 \operatorname{sign}\{\mathbf{v}^H \mathbf{u}\}$$

$$\mathbf{n} \leftarrow \rho \mathbf{v} + \rho^* \mathbf{S}^H \mathbf{u}$$

$$v = ||\mathbf{u}|| \quad (L_2 \text{ norm})$$

 $\mathbf{u} \leftarrow \mathbf{u}/\mathbf{v}$

Compute output SINR measurement χ



Compute phase-shift estimate φ ,

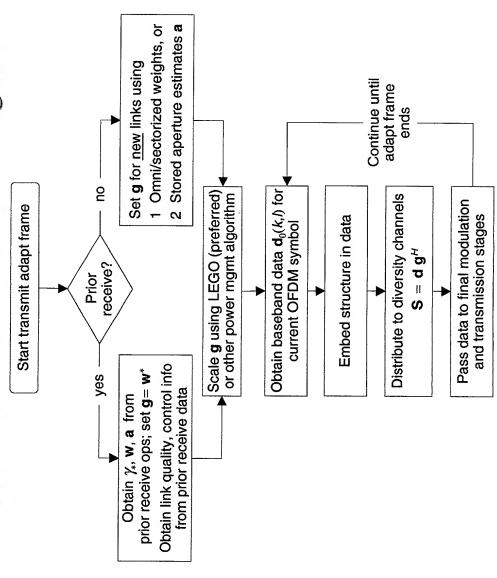
Compute slot/diversity combined output data

$$\mathbf{d} = \mathbf{Q}_0 \mathbf{u} + (\mathbf{Q}_1 \mathbf{u}) e^{-j \phi}$$

Compute unwhitened combiner weights w, aperture vector a, $\mathbf{w} = \mathbf{C}\mathbf{u}$

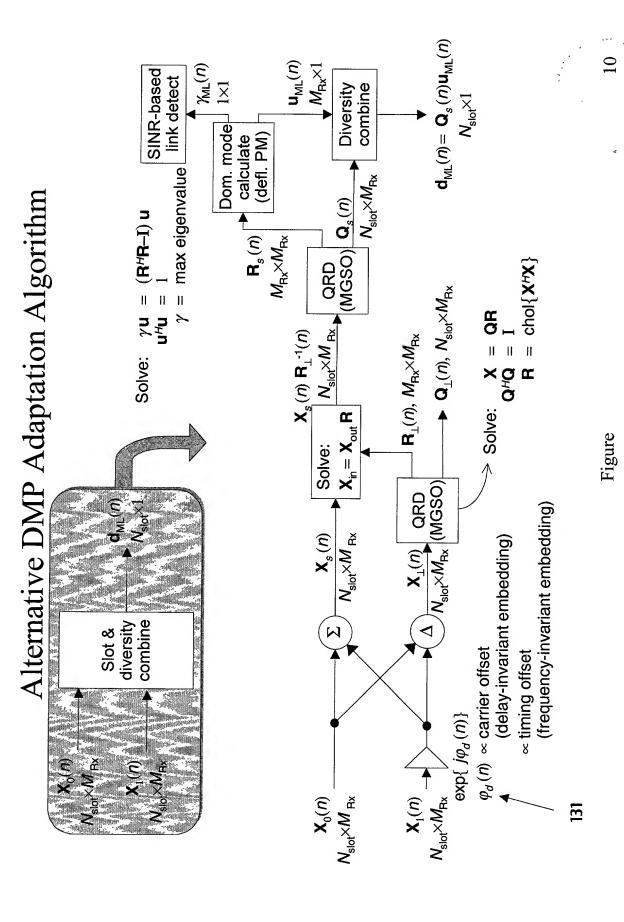
$$= \mathbf{C}\mathbf{u}$$
$$= \mathbf{p}^{H}\mathbf{u}$$

Single-Link Transmitter Flow Diagram



Figure

6



alternative converging embedded-signal-differentiation

algorithms

Dominant-Mode Prediction

Solve: $\gamma \mathbf{u} = (\mathbf{R}^H \mathbf{R} - \mathbf{I}) \mathbf{u}$ $||\mathbf{u}|| = 1 (\mathbf{L}_2 \text{ norm})$ $\gamma = \text{max eigenvalue}$

Optimization Algorithm

Initialize: $\mathbf{u} = r(M,M)[r^*(M,1)-1]$ $\gamma = ||\mathbf{u}|| (L_2 \text{ norm})$ $\mathbf{u} \leftarrow \mathbf{u}/\gamma$

Iterate:

 $\begin{array}{ll}
\mathbf{u} \leftarrow \mathbf{R}^H \mathbf{v} - \mathbf{u} \\
\gamma = \|\mathbf{u}\| \ (L_2 \text{ norm}) \\
\mathbf{u} \leftarrow \mathbf{u}/\gamma
\end{array}$

Auto-SCORE

 $v(\phi) \mathbf{u} = \mathbf{S}(\phi) \mathbf{u}$ $\mathbf{S}(\phi) = 1/2(\mathbf{S}\phi^{j\phi} + \mathbf{S}^{H}\phi^{-j\phi})$ $||\mathbf{u}|| = 1 \ (L_2 \text{ norm})$

Solve:

 $v(\phi) = \max \text{ eigenvalue}$

 $\varphi = \arg \max_{\varphi} v(\varphi)$

Optimization Algorithm

Initialize:

 $\mathbf{u} = [s(m, M)]$ $v = ||\mathbf{u}|| (L_2 \text{ norm})$ $\mathbf{u} \leftarrow \mathbf{u}/v$

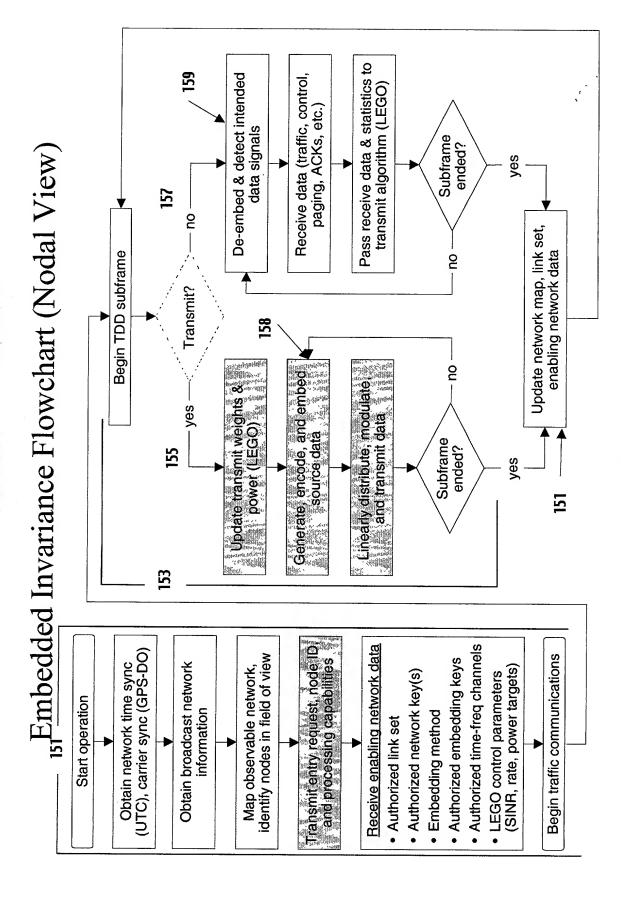
Iterate:

 $\rho = 1/2 \operatorname{sign}\{\mathbf{v}^{H}\mathbf{u}\}$ $n_{H}S*d + vd \rightarrow n$ ns = >

 $v = ||\mathbf{u}|| \quad (L_2 \text{ norm})$ $\mathbf{u} \leftarrow \mathbf{u}/v$

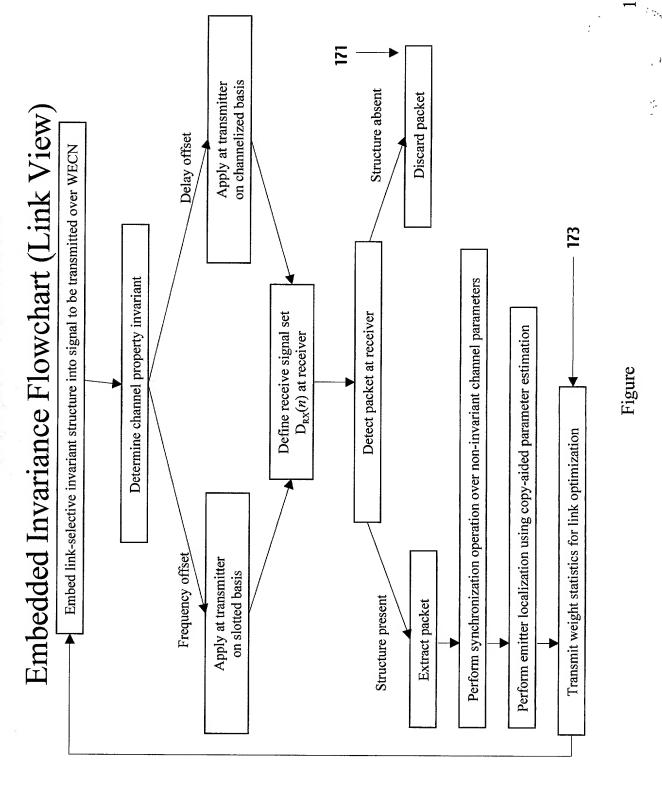
Finalize:

 $\varphi = \arg\{\rho\}$ $\gamma = \nu/(1-\nu)$

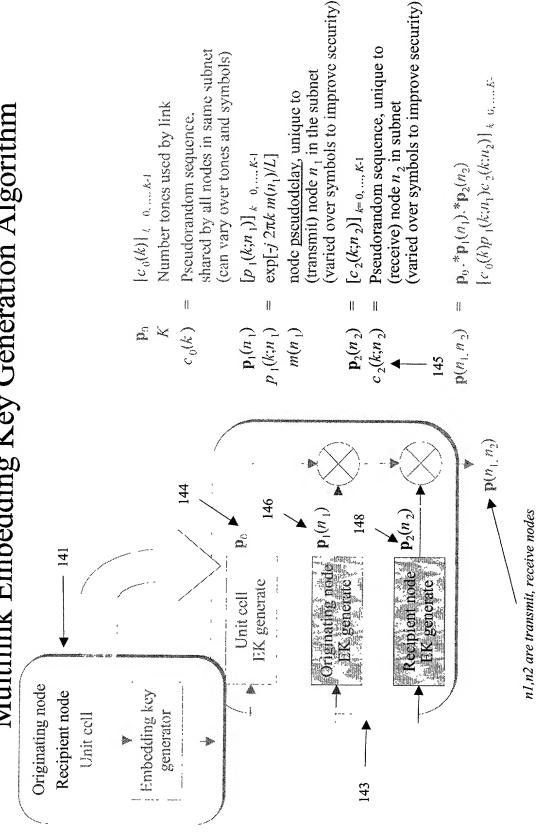


Figure

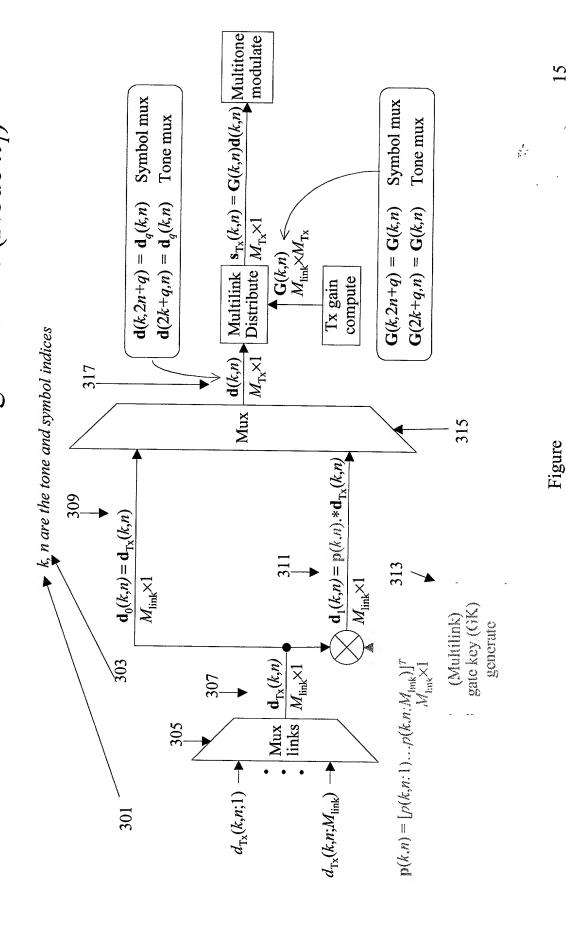
12

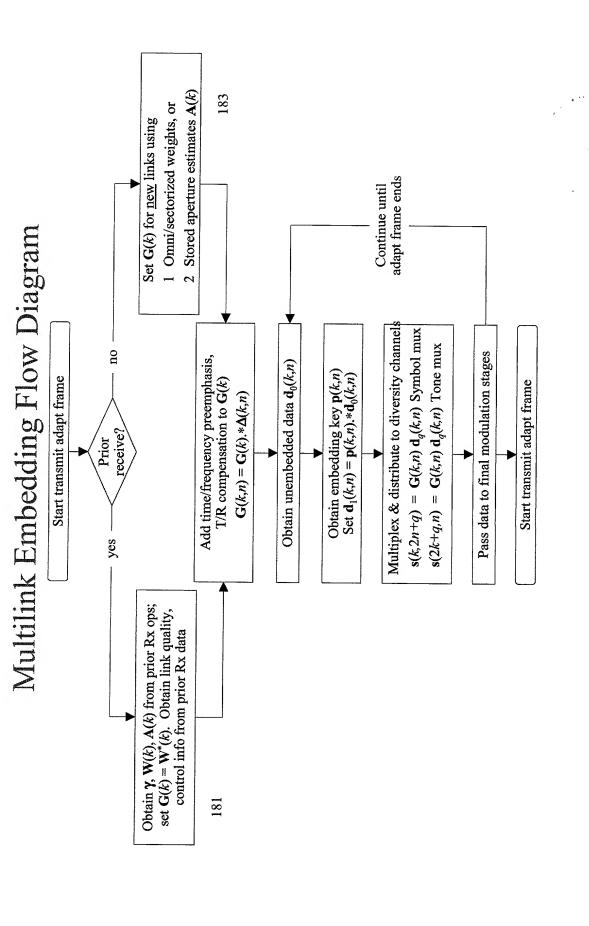


Multilink Embedding Key Generation Algorithm

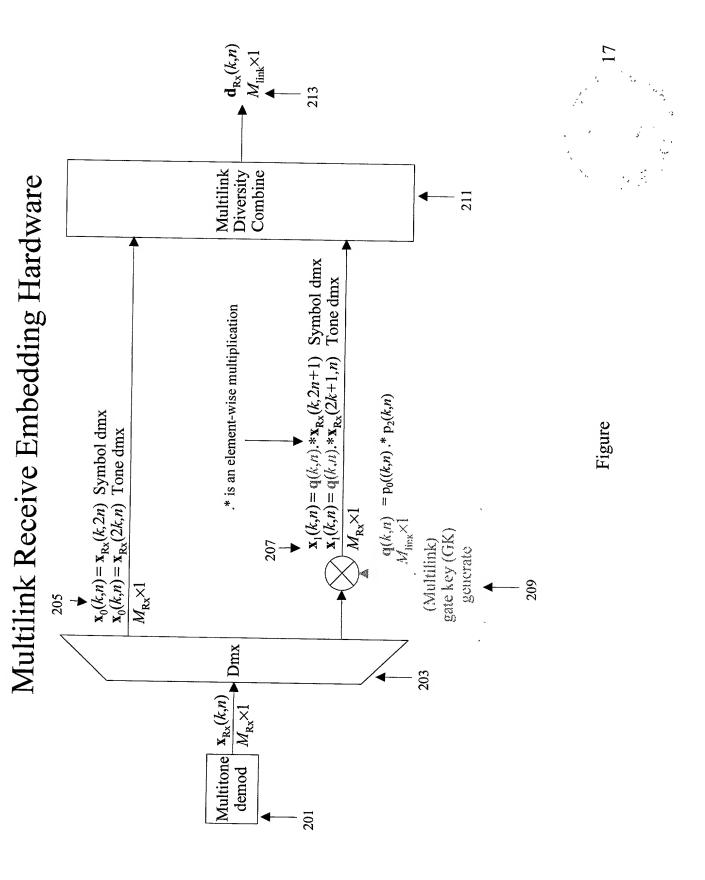


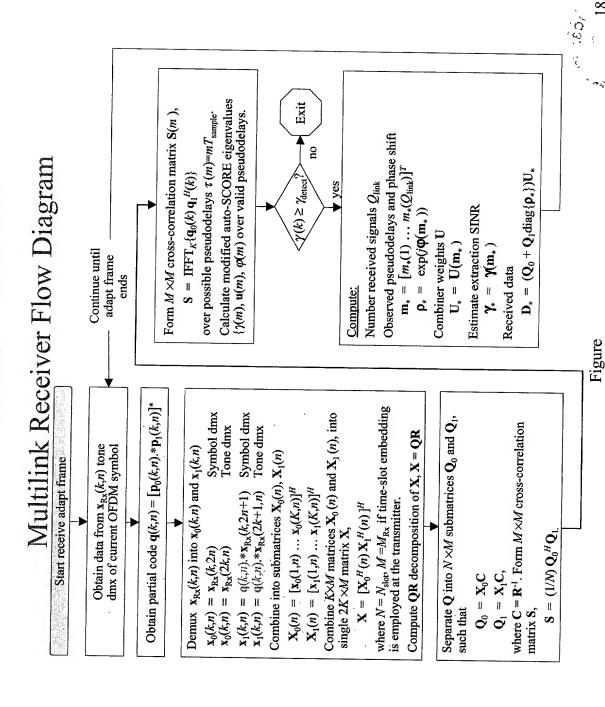
Multilink Transmit Embedding Hardware (Node n_1)



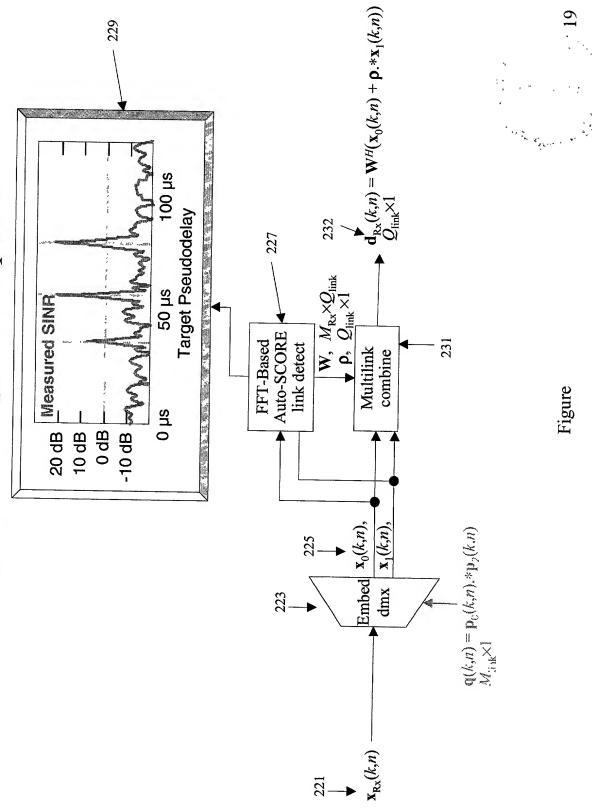


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Link Detection, Separation Operation



Pseudodelay Plots and Antenna Patterns

